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CERTIFICATE OF MAILING												
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date:												
Turned on Printed Name				ERIC S. HYMAN, ESQ.								
Typed or Printed Name	EKIC S. HYN	IAN,	ESQ.									

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## TRADEMAN Complete If Known 08/887,680 FEE TRANSMITTAL Application Number 7/3/97 Filing Date Note: Effective October 1, 1997 First Named Inventor Powell Patent fees are subject to annual revision 2783 Group Art Unit Eng Examiner Name

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<sup>\*</sup>Highest number of claims previously paid for if an amendment is being transmitted.



Our Ref. No.: 82225.P0189R

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

M. Powell et al.

Serial No.: 08/887,680

Filed: July 3, 1997

For: METHOD AND APPARATUS FOR

**EXTENDING COMPUTER** 

ARCHITECTURE FROM 32 TO 64 BITS

Examiner: D. Eng

Art Group: 2783

RECEIVEL JUL 24 2000

## RESPONSE TO OFFICE ACTION

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Sir:

In an Office Action mailed July 20, 2000, it is noted that the Amendment filed April 10, 2000 proposes amendments to Claim 33 that do not comply with 37 CFR 1.121(b). It is submitted that the following Amendment is responsive to this requirement.

## IN THE CLAIMS

- 1 33. (Amended) A method for context switching a processor that executes procedures
- 2 having differing word sizes, comprising the steps of:
- 3 testing a least significant bit of a stack pointer register in the processor that indicates whether a set
- of data values for a procedure that are stored in a set of registers in the processor, each have a
  - first word size or a second word size, wherein the first word size is less than the second word
- 6 <u>size</u>;
- 7 transferring the data values from a least significant portion of each register to a first stack save area
- 8 in memory if the least significant bit of the stack pointer register indicates the first word size;
- 9 transferring the data values from the registers to a second stack save area in memory if the least
- significant bit of the stack pointer register indicates the second word size[;